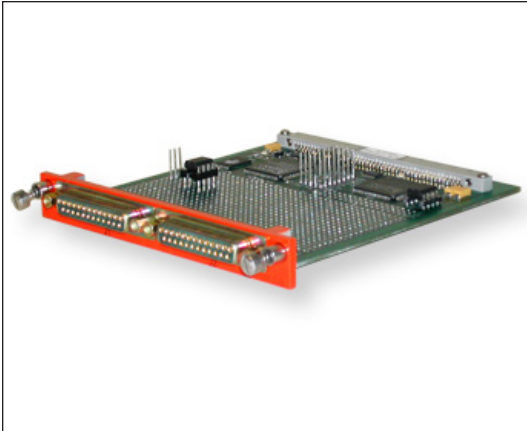


### User Development/Breadboard Card



#### Applications

- Development board for user specific application
- CDAU/EDAU breadboard card
- Quick turnaround for small unique application

#### Features

- User development card for EDAU/CDAU chassis
- Includes FPGA for Internal Bus interface
- Includes User programmable FPGA
- Has user breadboard area
- User FPGA has access to internal IRIG Time Bus
- Available 16 EEPROM programmable words for user settings
- Provides two 25 pin user configurable connectors
- Provides access to unit supply power with resettable fuses
- Compatible with EDAU/CDAU-20XX product family

#### Description

The USR-100T card provides the user with the capability to develop design for use in the CDAU/EDAU-20XX chassis. The card has two FPGA Xilinx devices, one FPGA interfaces with the chassis's bus, and the other FPGA is user programmable. The board includes a large breadboard area for prototype development with access to the user FPGA I/O, the card's supply, and two 25 pin external card connectors. The USR-100T-2 provides the ability to sample System Time words.

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#### USR-100/USR-100T Datasheet

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CAIS  
Compatible



Management  
System  
AS9100C  
ISO 9001:2008

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